PATENT & TRADEMARK OFFICE
MAR 25 2004
OIPE

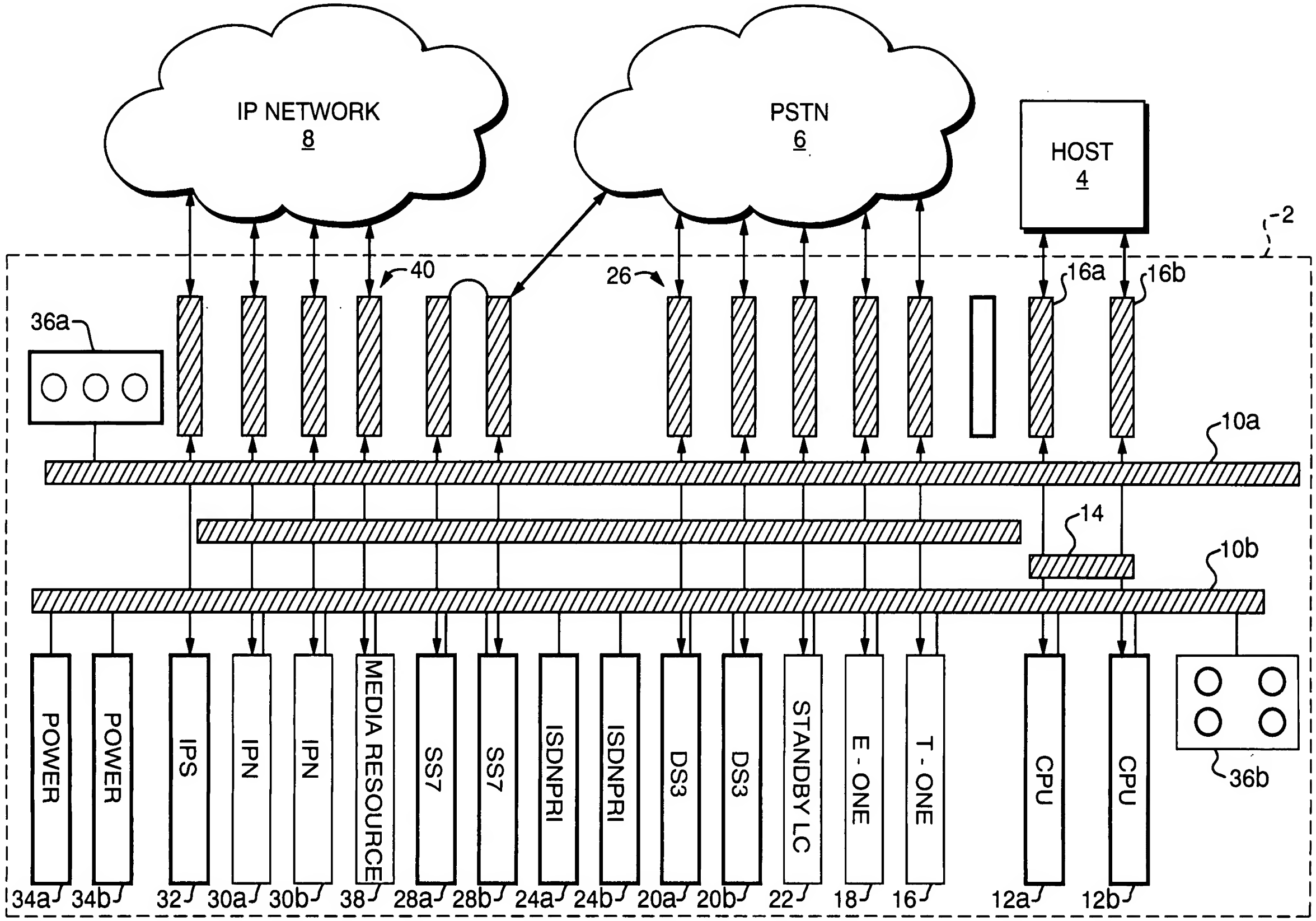


FIG. 1

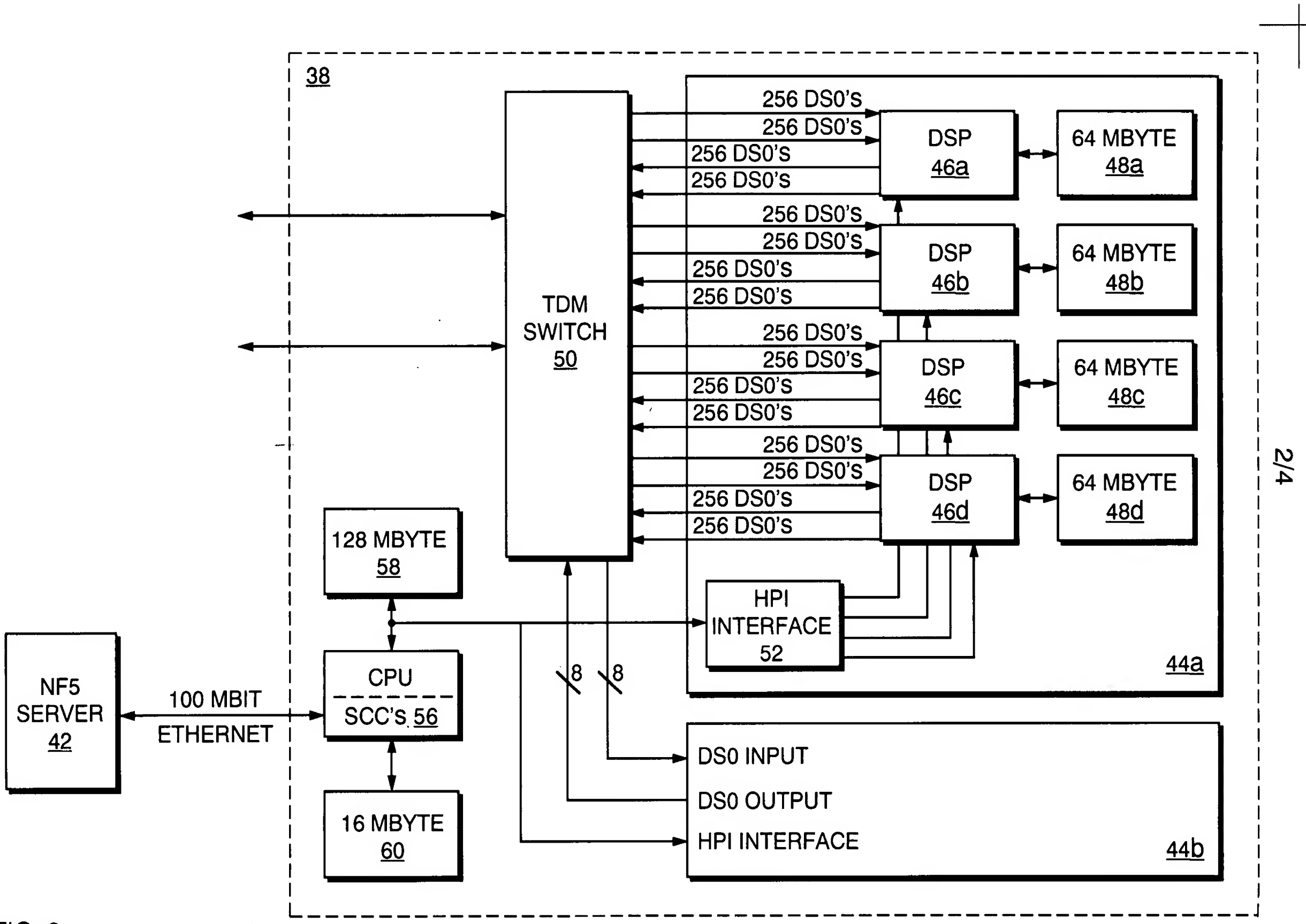
38
256 DS0's
256 DS0's
256 DS0's
256 DS0's
DSP
46a
64 MBYTE
48a
256 DS0's
256 DS0's
256 DS0's
256 DS0's
DSP
46b
64 MBYTE
48b
TDM
SWITCH
50
256 DS0's
256 DS0's
256 DS0's
256 DS0's
DSP
46c
64 MBYTE
48c
256 DS0's
256 DS0's
256 DS0's
256 DS0's
DSP
46d
64 MBYTE
48d
128 MBYTE
58
HPI
INTERFACE
52
44a
8
8
NF5
SERVER
42
100 MBIT
ETHERNET
CPU
SCC's 56
16 MBYTE
60
DS0 INPUT
DS0 OUTPUT
HPI INTERFACE
44b

FIG. 2

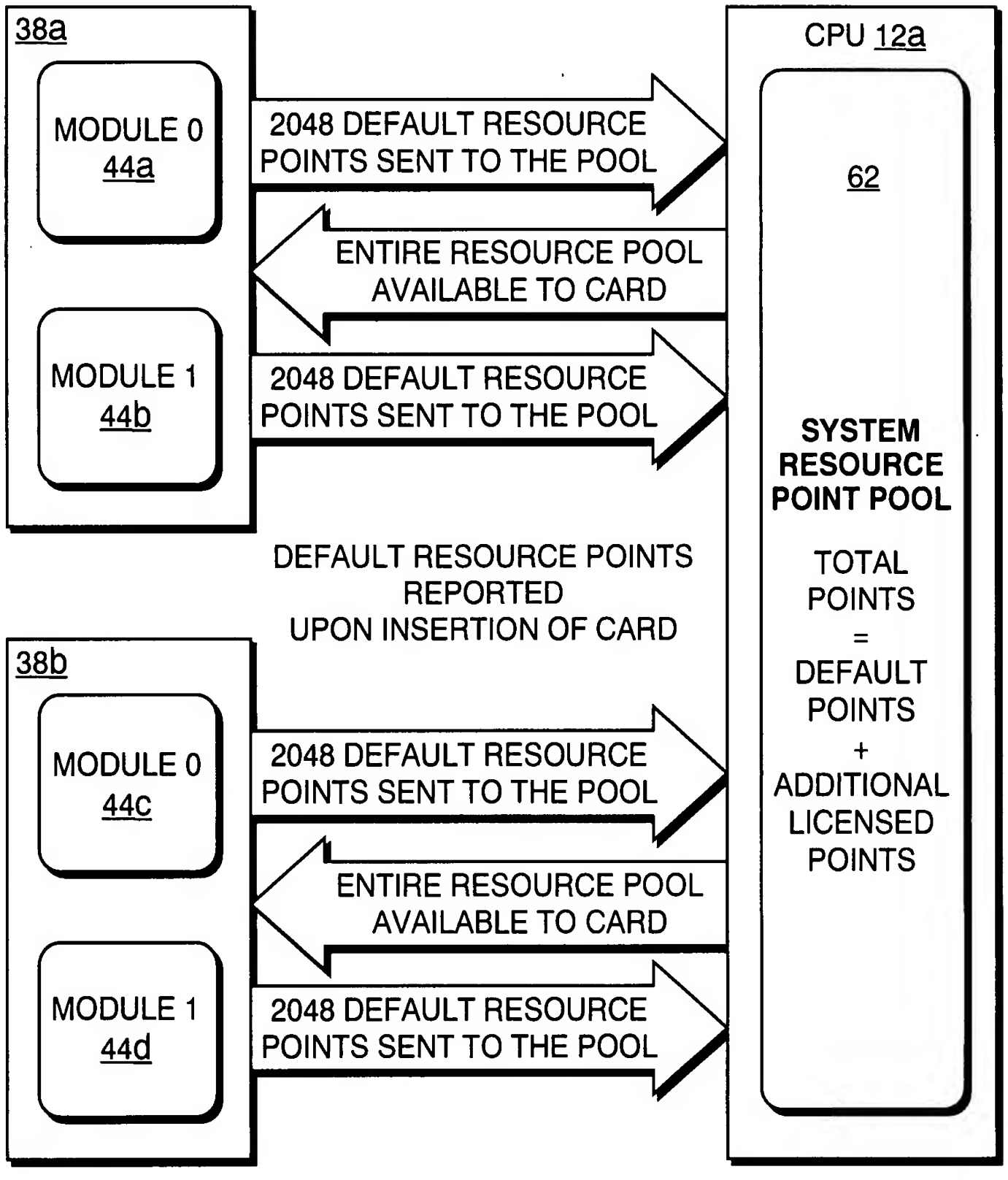
38a
MODULE 0
44a
MODULE 1
44b
2048 DEFAULT RESOURCE POINTS SENT TO THE POOL
ENTIRE RESOURCE POOL AVAILABLE TO CARD
2048 DEFAULT RESOURCE POINTS SENT TO THE POOL
DEFAULT RESOURCE POINTS REPORTED UPON INSERTION OF CARD
38b
MODULE 0
44c
MODULE 1
44d
2048 DEFAULT RESOURCE POINTS SENT TO THE POOL
ENTIRE RESOURCE POOL AVAILABLE TO CARD
2048 DEFAULT RESOURCE POINTS SENT TO THE POOL
CPU 12a
62
SYSTEM RESOURCE POINT POOL
TOTAL POINTS = DEFAULT POINTS + ADDITIONAL LICENSED POINTS

FIG. 3

| NUMBER | FUNCTION | MAX CHANNELS PER DSP STREAM | MAX CHANNELS PER DSP CHIP | # OF RESOURCE POINTS/ CHANNEL |
|---|---|---|---|---|
| TONE RECEPTION | | | | |
| 0x01 | DTMF (μ-law) | 192 | 384 | 5 |
| 0x02 | MFR1 (μ-law) | 256 | 512 | 5 |
| 0x03 | DTMF (A-law) | 192 | 384 | 5 |
| 0x04 | MFR1 (A-law) | 256 | 512 | 5 |
| 0x05 | MFR2 (A-law) | 256 | 512 | 8 |
| 0x06 | MFR2 (μ-law) | 256 | 512 | 8 |
| 0x07 | CPA (A-law) | 192 | 384 | 10 |
| 0x08 | CPA (μ-law) | 192 | 384 | 10 |
| 0x09 | DIAL PULSE | 192 | 384 | 10 |
| 0x0A | ENERGY DETECTION | 192 | 384 | 10 |
| TONE GENERATION | | | | |
| 0x30 | UNIVERSAL GEN. (μ-law) | 256 | 512 | 0 |
| 0x31 | UNIVERSAL GEN. (A-law) | 256 | 512 | 0 |

FIG. 4A

| NUMBER | FUNCTION | MAX CHANNELS PER DSP STREAM | MAX CHANNELS PER DSP CHIP | # OF RESOURCE POINTS/ CHANNEL |
|---|---|---|---|---|
| CONFERENCING (THESE FUNCTIONS REQUIRE 2 STREAMS) | | | | |
| 0x21 | MONITOR | 128 | 256 | 8 |
| 0x22 | UNIFIED | 128 | 256 | 8 |
| 0x23 | DTMF CLAMPED | 128 | 256 | 8 |
| 0x24 | DYNAMIC (μ-law) | 128 | 256 | 8 |
| 0x25 | DYNAMIC w/DTMF CLAMPED (μ-law) | 128 | 256 | 8 |
| 0x26 | DYNAMIC (A-law) | 128 | 256 | 8 |
| 0x27 | DYNAMIC w/DTMF CLAMPED (A-law) | 128 | 256 | 8 |
| FILE PLAYBACK\RECORD (THESE FUNCTIONS REQUIRE 2 STREAMS) | | | | |
| 0x1D | FILE PLAYBACK/RECORD | 64 | 128 | 12 |

FIG. 4B